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July 28, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,961,278
Issued: November 1, 2005
For: SYNCHRONOUS SELF REFRESH EXIT CONTROL METHOD
AND CIRCUIT IN SEMICONDUCTOR MEMORY DEVICE
Inventor: Jeong
Our File No. 051876.P544

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Eric S. Hyman
Reg. No. 30,139

ESH/nsf
Enclosures:
PTO Form- 1449

Certificate
AUG 23 2006
of Correction

2006 AUG 23 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,961,278

DATED: November 1, 2005

INVENTOR(S): Jeong

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim #1, Last Line, after " unit" please insert - - 100A - -.

MAILING ADDRESS OF SENDER:

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
12400 Wilshire Blvd. 7th floor
Los Angeles, CA 90025-1030

PATENT NO. 6,961,278

Certificate of Correction (PTO Form 1050)-Amended

1 of 1

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